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	Application No.	Applicant(s)	
Notice of Allowability	10/789,642 Examiner	HAYAT-DAWOODI, P	CAMBIZ
	Russell M. Kobert	2829	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in to their appropriate commures (RIGHTS). This application is su	this application. If not included nication will be mailed in due c	d ourse. <b>THIS</b>
1. $igtimes$ This communication is responsive to $the$ Election filed FeI	<u>bruary 24, 2005</u> .		
2. 🛮 The allowed claim(s) is/are <u>10-17</u> .			
3. $igotimes$ The drawings filed on <u>01 March 2004</u> are accepted by the	Examiner.		
4. Acknowledgment is made of a claim for foreign priority of a) All b) Some* c) None of the:  1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDON! THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. A SUBSTITUTE OATH OR DECLARATION must be submined in the complex of the complex	re been received. re been received in Application occuments have been received re of this communication to file a MENT of this application.  mitted. Note the attached EXAM res reason(s) why the oath or out the submitted. rson's Patent Drawing Review r's Amendment / Comment or in 1.84(c)) should be written on the the header according to 37 CFR	No in this national stage application this national stage application areply complying with the requirement of the MENT of Notice action of the drawings in the front (not the late 1.121(d).	uirements DTICE OF
attached Examiner's comment regarding REQUIREMENT			
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5. 🗍 Notice of Info	ormal Patent Application (PTO	-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🔲 Interview Sur	• • • • • • • • • • • • • • • • • • • •	·
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 0304</li> <li>Examiner's Comment Regarding Requirement for Deposit</li> </ol>	/08), 7. ⊠ Examiner's A	nall Date Amendment/Comment Statement of Reasons for Allov	vance
of Biological Material	9.	.*.	

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## Examiner's Amendment

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be

submitted no later than the payment of the issue fee.

2. Applicant's election of species I, Figure 3, claims 10-17, in the reply filed on

February 24, 2005 is acknowledged. Because applicant did not distinctly and

specifically point out the supposed errors in the restriction requirement, the election has

been treated as an election without traverse (MPEP § 818.03(a)).

3. Claims 1-9 and 18-21 are withdrawn from further consideration pursuant to 37

CFR 1.142(b) as being drawn to a nonelected species, there being no allowable generic

or linking claim. Election was made without traverse in the reply filed on February 24,

2005.

4. This application is in condition for allowance except for the presence of claims 1-

9 and 18-21 to species non-elected without traverse. Accordingly, claims 1-9 and 18-21

have been cancelled.

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5. On page 1, prior to the "FIELD OF THE INVENTION," the following has been

inserted:

-- CROSS-REFERENCE TO RELATED APPLICATIONS

This application is a divisional of U.S. Patent Application No. 09/873,057, filed June 2, 2001, now U.S. Patent No. 6,853,178. --

Reasons for Allowance

6. The following is an examiner's statement of reasons for allowance:

A semiconductor device comprising:

- A leadframe with a chip mount pad having at least one slit
- An integrated circuit chip
- The passive surface of the integrated circuit chip attached to the mount pad by a polymeric material
- The active surface of the integrated circuit chip having a Hall structure including current and voltage terminals integrated into the circuit

As further described in claim 10 has not been found.

It is further noted that the examiner's reasons are understood to be predicated upon consideration of each of the claims as a whole, and not upon any specific elements of the claims.

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Any comments considered necessary by applicant must be submitted no later

than the payment of the issue fee and, to avoid processing delays, should preferably

accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

7. The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

Engel et al (5963028) shows a leadframe, a Hall effect sensor unit, a die attach

pad, current and voltage terminals integrated into the Hall effect sensor. However

Engel et al fails to disclose an integrated circuit chip attached to a mount pad by

polymeric material.

Goto et al (6759841) shows a Hall-Effect current detector in the form of a

semiconductor chip mounted to a metal-made mounting plate.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Russell Kobert whose telephone number is (571) 272-1963. The Examiner's Supervisor, Nestor R. Ramirez, can be reached at (571) 272-2034. For an automated menu of Tech Center 2800 phone numbers call (571) 272-

2800.

Russell M. Kobert Patent Examiner

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March 16, 2005

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03/18/05